

FEATURES:

- STANDARD 16-PIN PACKAGE
- 5 EQUALLY SPACED DELAY TAPS
- ECL 10K INTERFACED
- DELAYS FROM 10ns-500ns
- POWER DISSIPATION:200mW TYP.(NO LOAD)

ELECTRICAL CHARACTERISTICS:

I _{IH} Logic"1" Input Current	:	265uA max
I _{IL} Logic"0" Input Current	:	-5uA max
V _{OH} Logic"1" Output Voltage	:	-0.96V min
V _{OL} Logic"0" Output Voltage	:	-1.65V max
V _{IH} Logic"1" Input Voltage	:	-0.98V min
V _{IL} Logic"0" Input Voltage	:	-1.63V max
TA Operating Temperature	:	0°C to 70°C
Supply Current	:	65mA Typ.

INPUT PULSE TEST CONDITION:

Pulse Voltage	:	-1V(-0.75V TO -1.75V)
Pulse Width	:	3T _d
Pulse Spacing	:	10T _d
Pulse Rise Time	:	2nS(20% to 80%)
Time Delay Measured	:	@-1.3V level
Supply Voltage V _{cc}	:	-0V
Supply Voltage V _{ee}	:	-5.2Vdc

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Delay Line
83A SERIES

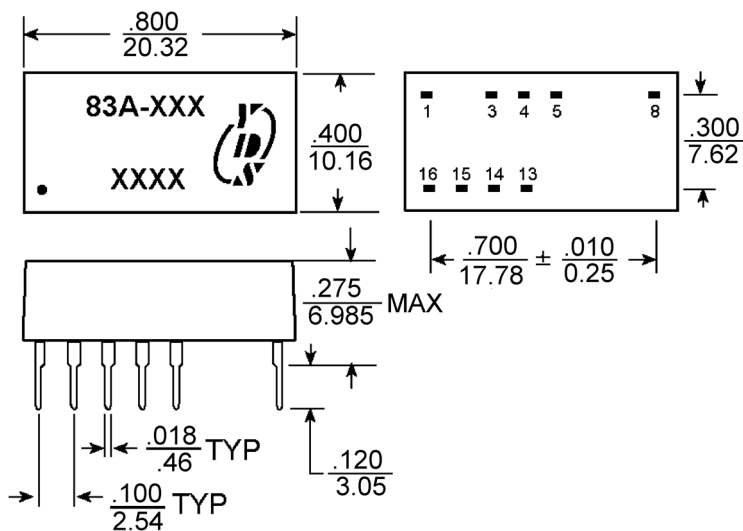
16 Pin Dual In-Line
ECL 10K
Programmable



ELECTRICAL SPECIFICATIONS

PART NO.	TOTAL DELAY (nS)	TAP DELAY (nS)	MAX OUTPUT RISETIME (nS)	MAX OUTPUT FALLTIME (nS)
83A-010NL	10±1	2.0	4.0	4.0
83A-020NL	20±1	4.0	4.0	4.0
83A-025NL	25±1.2	5.0	4.0	4.0
83A-050NL	50±2.5	10.0	4.0	4.0
83A-075NL	75±3.7	15.0	5.0	5.0
83A-100NL	100±5	20.0	6.0	6.0
83A-150NL	150±7.5	30.0	7.0	7.0
83A-200NL	200±10	40.0	8.0	8.0
83A-250NL	250±12.5	50.0	10.0	10.0
83A-300NL	300±15	60.0	15.0	15.0
83A-400NL	400±20	80.0	15.0	15.0
83A-500NL	500±25	100.0	20.0	20.0

Markings and Dimensions



Dimensions: inches/mm

Unless otherwise specified, all tolerances are ±.010/±0.25

Pin Connections

