

## FEATURES:

- 14-PIN PACKAGE
- 10 EQUALLY-SPACED TAPS
- TTL SCHOTTKY INTERFACED
- TOTAL DELAYS FROM 25-1000nS

## ELECTRICAL CHARACTERISTICS:

I <sub>IH</sub> Logic"1" Input Current	:	100uA max
I <sub>IL</sub> Logic"0" Input Current	:	-4mA max
V <sub>OH</sub> Logic"1" Output Voltage	:	2.4V min
V <sub>OL</sub> Logic"0" Output Voltage	:	0.4V max
V <sub>IH</sub> Logic"1" Input Voltage	:	2.0V min
V <sub>IL</sub> Logic"0" Input Voltage	:	0.8V max
T <sub>A</sub> Operating Temperature	:	0°C to 70°C
N <sub>H</sub> Fanout"1" Output	:	20 TTL Load
N <sub>L</sub> Fanout"0" Output	:	10 TTL Load
Supply Current	:	150mA TYP.

## INPUT PULSE TEST CONDITION:

Pulse Voltage	:	3.2V
Pulse Width	:	100%T <sub>d</sub>
Duty Cycle	:	T <sub>d</sub> + 3T <sub>d</sub>
Pulse Rise Time	:	2nS(0.75 TO 2.4V)
Time Delay Measured	:	@1.5V level
Supply Voltage V <sub>cc</sub>	:	5.0±0.25Vdc

YUAN DEAN SCIENTIFIC



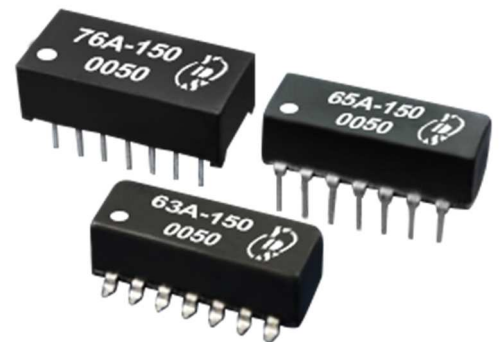
Delay Line

63A/65A/76A SERIES

14 Pin

Leading and Trailing

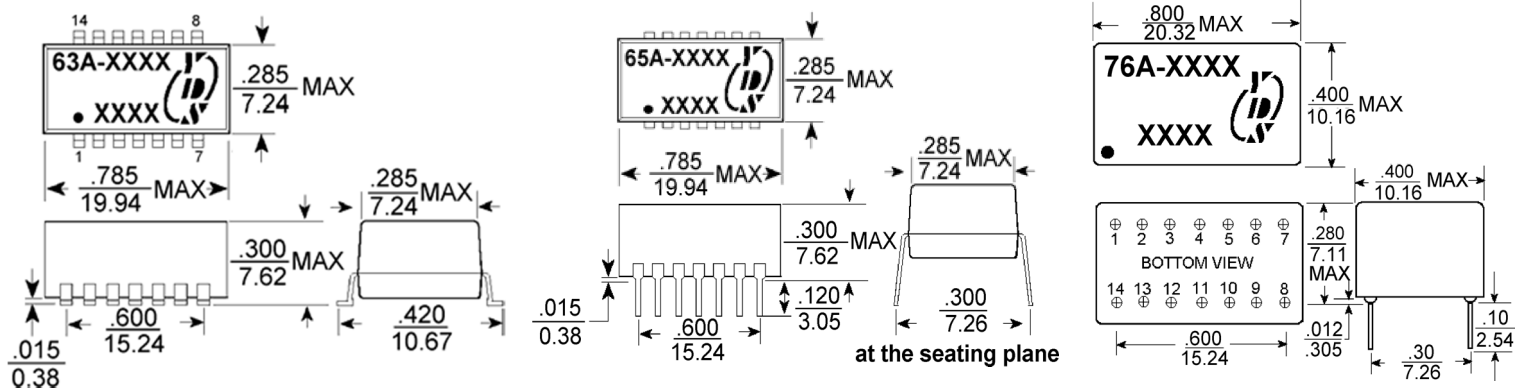
TTL Active



**ELECTRICAL SPECIFICATIONS**

PART NO.	DELAY TIME Td(nS)	TAP DELAY (nS)	RISE TIME Tr(nS)max
63A/65A/76A-025NL	25±2nS	2.5±1	4
63A/65A/76A-050NL	50±5%	5±2	4
63A/65A/76A-075NL	75±5%	7.5±2	4
63A/65A/76A-100NL	100±5%	10±2	4
63A/65A/76A-150NL	150±5%	15±2	4
63A/65A/76A-200NL	200±5%	20±2	4
63A/65A/76A-250NL	250±5%	25±5%	4
63A/65A/76A-300NL	300±5%	30±5%	4
63A/65A/76A-500NL	500±5%	50±5%	4
63A/65A/76A-1000NL	1000±5%	100±5%	4

**Markings and Dimensions**



Dimensions: inches/mm Unless otherwise specified, all tolerances are ±.010/±0.25

**Pin Connections**

