

## FEATURES:

- 16-PIN PACKAGE
- 5EQUALLY-SPACED TAPS
- TTL SCHOTTKY INTERFACED
- TOTAL DELAYS FROM 25-1000nS

YUAN DEAN SCIENTIFIC



## ELECTRICAL CHARACTERISTICS:

I <sub>IH</sub> Logic"1" Input Current	:	50uA max
I <sub>IL</sub> Logic"0" Input Current	:	-2mA max
V <sub>OH</sub> Logic"1" Output Voltage	:	2.7V min
V <sub>OL</sub> Logic"0" Output Voltage	:	0.5V max
V <sub>IH</sub> Logic"1" Input Voltage	:	2.0V min
V <sub>IL</sub> Logic"0" Input Voltage	:	0.8V max
T <sub>A</sub> Operating Temperature	:	0°C to 70°C
N <sub>H</sub> Fanout"1" Output	:	20 TTL Load
N <sub>L</sub> Fanout"0" Output	:	10 TTL Load
Supply Current	:	75mA TYP.

Delay Line

51A SERIES

16 Pin

Dual In-Line

TTL Active

## INPUT PULSE TEST CONDITION:

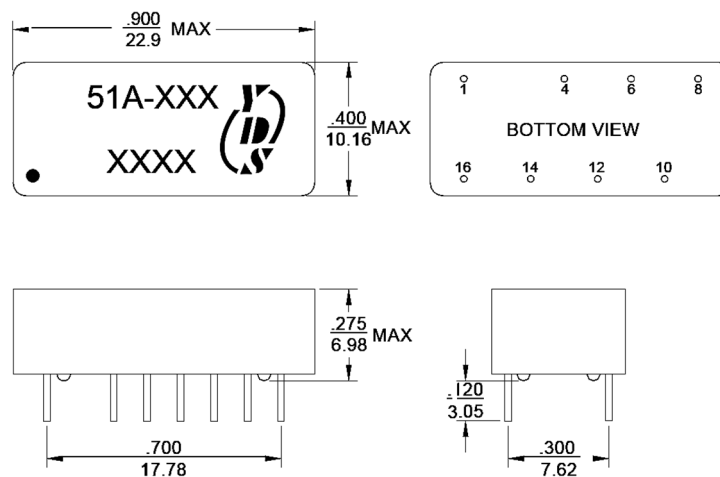
Pulse Voltage	:	3.2V
Pulse Width	:	50nS Min. or 100%T <sub>d</sub>
Duty Cycle	:	<50%
Pulse Rise Time	:	2nS(0.75 TO 2.4V)
Time Delay Measured	:	@1.5V level
Supply Voltage V <sub>cc</sub>	:	5.0±0.25Vdc



## ELECTRICAL SPECIFICATIONS

PART NO.	DELAY TIME Td(nS)	TAP DELAY (nS)	RISE TIME Tr(nS)max
51A-025NL	25±2	5±2	4
51A-050NL	50±5%	10±2	4
51A-075NL	75±5%	15±2	4
51A-100NL	100±5%	20±2	4
51A-150NL	150±5%	30±2	4
51A-200NL	200±5%	40±2	4
51A-250NL	250±5%	50±5%	4
51A-300NL	300±5%	60±5%	4
51A-500NL	500±5%	100±5%	4
51A-1000NL	1000±5%	200±5%	4

## Markings and Dimensions



Dimensions: inches/mm Unless otherwise specified, all tolerances are  $\pm 0.10/\pm 0.25$

## Pin Connections

