

## FEATURES:

- 14-PIN PACKAGE
- 5EQUALLY-SPACED TAPS
- FAST TTL SCHOTTKY INTERFACED
- TOTAL DELAYS FROM 25-1000nS
- TPLH AND TPHL CONTROLLED

## ELECTRICAL CHARACTERISTICS:

I <sub>IH</sub> Logic"1" Input Current	:	20uA max
I <sub>IL</sub> Logic"0" Input Current	:	-1.6mA max
V <sub>OH</sub> Logic"1" Output Voltage	:	2.5V min
V <sub>OL</sub> Logic"0" Output Voltage	:	0.5V max
V <sub>IH</sub> Logic"1" Input Voltage	:	2.0V min
V <sub>IL</sub> Logic"0" Input Voltage	:	0.8V max
T <sub>A</sub> Operating Temperature	:	0°C to 70°C
N <sub>H</sub> Fanout"1" Output	:	20 TTL Load
N <sub>L</sub> Fanout"0" Output	:	10 TTL Load
Supply Current	:	50mA TYP.

## INPUT PULSE TEST CONDITION:

Pulse Voltage	:	3.2V
Pulse Width	:	50nS Min. or 100%T <sub>d</sub>
Duty Cycle	:	<50%
Pulse Rise Time	:	2nS(0.75 TO 2.4V)
Time Delay Measured	:	@1.5V level
Supply Voltage V <sub>cc</sub>	:	5.0±0.25Vdc

YUAN DEAN SCIENTIFIC



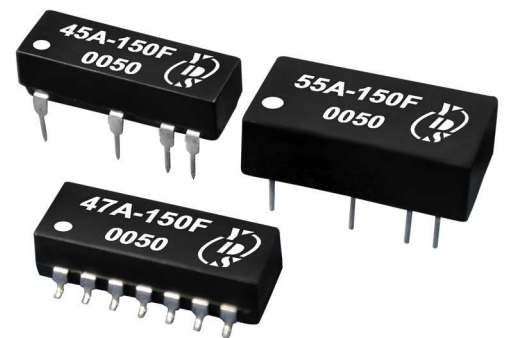
Delay Line

45AF/47AF/55AF SERIES

14 Pin

Leading and Trailing

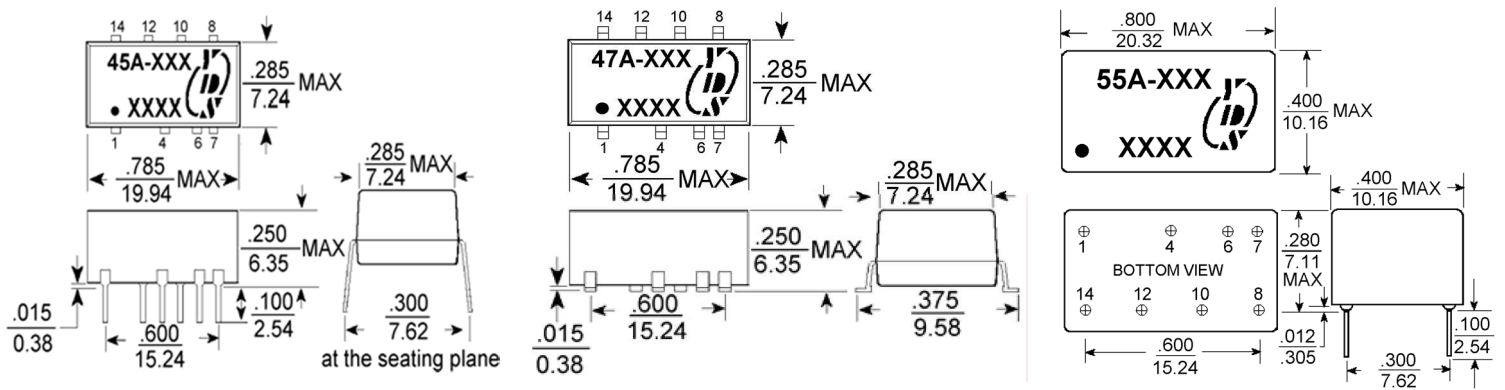
Fast TTL Active



**ELECTRICAL SPECIFICATIONS**

PART NO.	DELAY TIME Td(nS)	TAP DELAY (nS)	RISE TIME Tr(nS)max
45A/47A/55A-025FNL	25±2	5±2	4
45A/47A/55A-050FNL	50±5%	10±2	4
45A/47A/55A-075FNL	75±5%	15±2	4
45A/47A/55A-100FNL	100±5%	20±2	4
45A/47A/55A-150FNL	150±5%	30±2	4
45A/47A/55A-200FNL	200±5%	40±2	4
45A/47A/55A-250FNL	250±5%	50±5%	4
45A/47A/55A-300FNL	300±5%	60±5%	4
45A/47A/55A-500FNL	500±5%	100±5%	4
45A/47A/55A-1000FNL	1000±5%	200±5%	4

**Markings and Dimensions**



Dimensions: inches/mm Unless otherwise specified, all tolerances are ±.010/±0.25

**Pin Connections**

