

FEATURES:

- 7-PIN SIP Package
- 5 equally-spaced taps
- Operating temperature range :0°C to +70°C
- Low output distortion:±10%
- Total delays from 5-100Ns
(other values available upon request)

ELECTRICAL CHARACTERISTICS:

Total Delay	:	5 to 100ns
Tap Delays	:	20% increments standard
Rise time	:	SEE TABLE
Characteristic Impedance	:	50 to 500Ω(±10%)
Attenuation	:	5%max
Distortion	:	10% max
Temperature Coefficient of Delay	:	100ppm/°C max

INPUT PULSE TEST CONDITION:

Pulse Voltage	:	2.5V
Pulse Width	:	5Td
Pulse Rise Time	:	2nS(10% to 90%)

YUAN DEAN SCIENTIFIC



Delay Line
20P SERIES
7 Pin
Single In-Line
Passive



ELECTRICAL SPECIFICATIONS

PART NO.	TOTAL DELAY (ns)	TAP DELAY (ns)	RISETIME MAX(ns)	DC RESISTANCE(Ω)MAX PER IMPEDANCE(Ω)	
				50	100
20P-005XXNL	5±0.5	1±0.3	2.0	0.3	0.5
20P-010XXNL	10±1.0	2±0.4	3.3	0.5	1.0
20P-020XXNL	20±1.5	4±0.6	6.0	0.8	1.5
20P-030XXNL	30±2.0	6±1.0	9.0	1.2	2.5
20P-040XXNL	40±2.5	8±1.5	12.0	1.8	4.0
20P-050XXNL	50±3.0	10±1.8	15.0	2.3	5.0
20P-060XXNL	60±3.5	12±2.0	18.0	2.5	-
20P-070XXNL	70±4.0	14±2.0	22.0	3.5	-
20P-080XXNL	80±4.5	16±2.0	24.0	4.2	-
20P-090XXNL	90±5.0	18±3.0	27.0	4.5	-
20P-100XXNL	100±5.0	20±3.0	28.0	5.2	-

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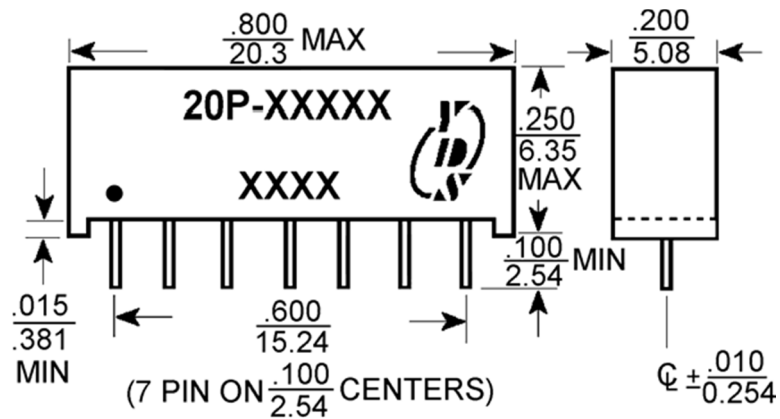


TEL: 886-6-3842899 FAX: 886-6-3843288

E-MAIL: ydsweb@yds.com.tw

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Markings and Dimensions



Dimensions: inches/mm Unless otherwise specified, all tolerances are $\pm .010/\pm 0.25$

Pin Connections

