

## FEATURES:

- 4-pin DIP and SMD package
- 1 equally-spaced taps
- Low cost prompt delivery
- Precision-grade internal inductor elements ensure excellent stability
- Operating temperature :0°C to +70°C

## ELECTRICAL CHARACTERISTICS:

Total Delay Tolerance	: ±20%
Temperature Coefficient of Delay	: 100ppm/°C max
Distortion	: ±10% Max
Dielectric Strength	: 100VDC
Insulation Resistance	: 1000MΩ min
Attenuation	: ±20% Max



YUAN DEAN SCIENTIFIC



Delay Line

12P/13P SERIES

4 Pin

DIP &amp; SMD

Single Output

Passive



## ELECTRICAL SPECIFICATIONS

RCD TYPE	DELAY TIME (ns)	RISETIME MAX(ns)	Available Impedance (Ω) Values(±10%)
12P/13P-0R1XXNL	0.1	2.0	50 or 75
12P/13P-0R2XXNL	0.2	2.0	50 or 75
12P/13P-0R3XXNL	0.3	2.0	50 or 75
12P/13P-0R4XXNL	0.4	2.0	50 or 75
12P/13P-0R5XXNL	0.5	2.0	50 or 75
12P/13P-0R6XXNL	0.6	2.0	50 or 75
12P/13P-0R7XXNL	0.7	2.0	50 or 75
12P/13P-0R8XXNL	0.8	2.0	50 or 75
12P/13P-0R9XXNL	0.9	2.0	50 or 75
12P/13P-001XXNL	1.0	2.0	50 or 75

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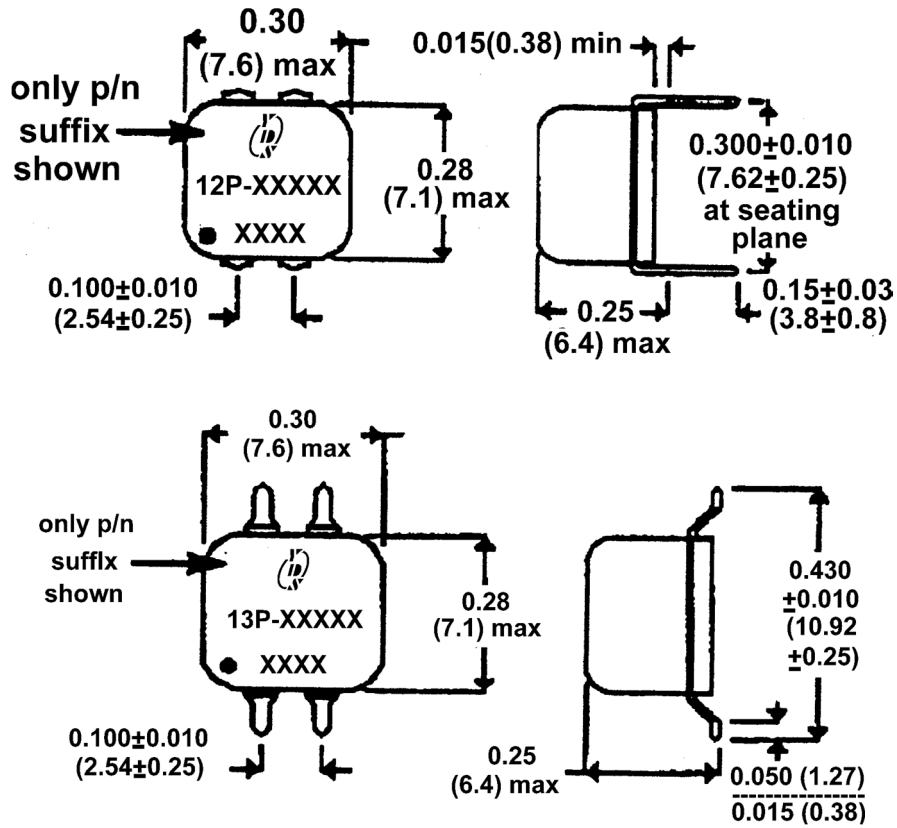


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Dimensions



Dimensions: inches/mm Unless otherwise specified, all tolerances are ±.010/±0.25

Schematics

