

FEATURES:

- Eight Transformers In A Surface Mount Package
- Includes IC Side Protection Circuit on each channel to suppress low voltage transients
- Compatible with PMC-Sierra's Comet chip (PM4354)
- Models Matched To Leading Quad And Dual T1/E1/CEPT/ISDN-PRI Transceivers
- Isolation Voltage: 1500Vrms.
- Recognized By UL 1950

TEST CONDITIONS:

- OCL ----- 100KHZ/100mV
- LL ----- 100KHZ/100mV
- Cww ----- 100KHZ/100mV



YUAN DEAN SCIENTIFIC



**T1/CEPT/ISDN-PRI
Transformers**

58T/58TB SERIES

Quad Port

SMT Package

With IC Side Protect



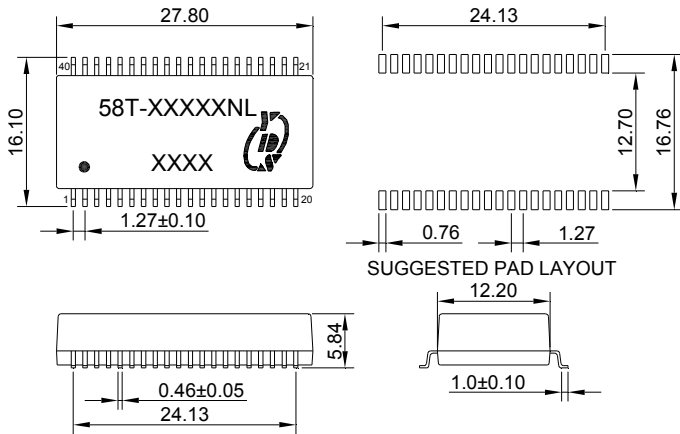
ELECTRICAL SPECIFICATIONS@25°C

Part Number		Turns Ratio (Pri:Sec±2%)		OCL@25°C (mH min.)		LL (uH MAX)		Cww (pF MAX)		Package Schematic	Primary Pins	
STD Temp.	EXT Temp.	Transmit	Receive	Transmit	Receive	Transmit	Receive	Transmit	Receive		Transmit	Receive
58T(B)-1221ANL	58T(B)-1221ACNL	1 : 1.36	1:1.36CT	1.2	1.2	0.6	0.6	35	35	A	1-2,6-7, 11-12,16-17	38-36,33-31, 28-26,23-21
58T(B)-1222ANL	58T(B)-1222ACNL	1 : 1.14	1 :1.14CT	1.2	1.2	0.6	0.6	35	35	A	1-2,6-7, 11-12,16-17	38-36,33-31, 28-26,23-21
58T(B)-1221CNL	58T(B)-1221CCNL	1 : 2CT	1 : 2CT	1.2	1.2	0.8	0.8	35	35	C	4-5,9-10, 14-15,19-20	24-25,29-30, 34-35,39-40
58T(B)-1221DNL	58T(B)-1221DCNL	1 : 2	1 : 2CT	1.2	1.2	0.6	0.6	35	35	D	1-2,9-10, 11-12,19-20	23-25,26-28, 33-35,36-38
58T(B)-1222CNL	58T(B)-1222CCNL	1 : 1.36CT	1 : 2CT	1.2	1.2	0.6	0.6	35	35	C	24-25,29-30, 34-35,39-40	4-5,9-10, 14-15,19-20

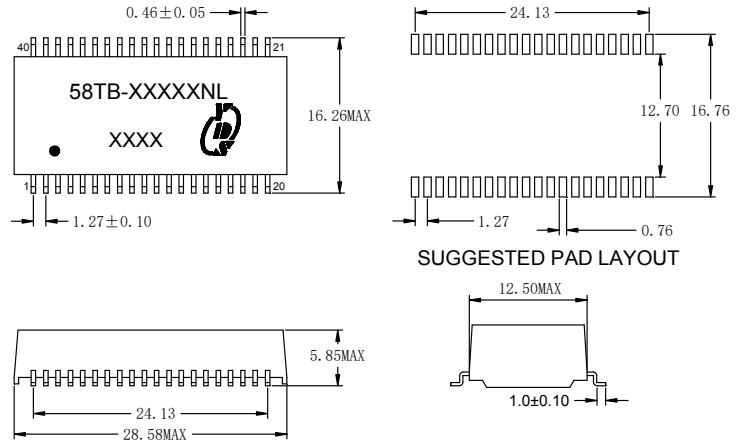


Mechanicals and Dimensions

58T (Molding)



58TB (Bottom Potting)



Schematics

